
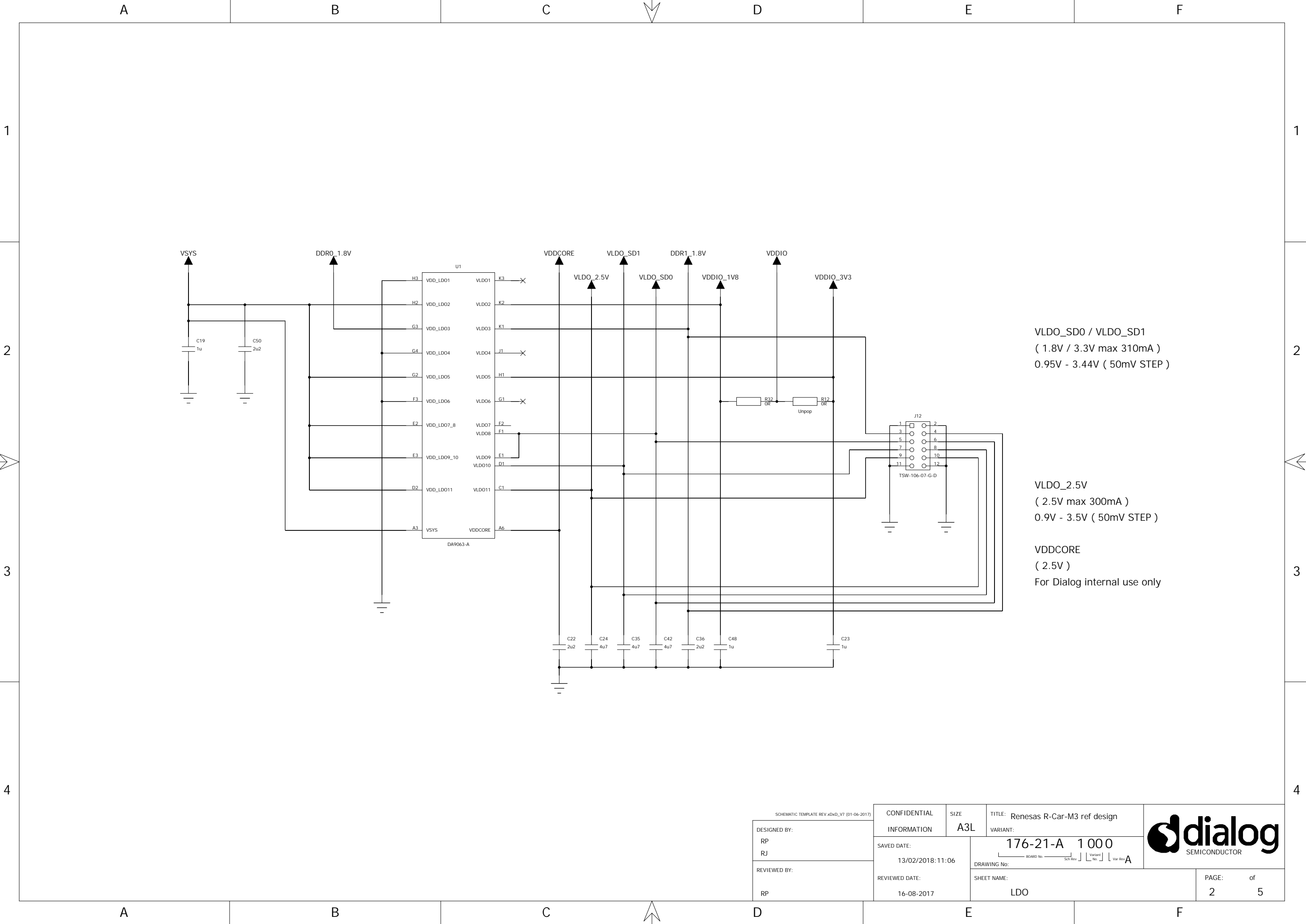


MECHANICAL PARTS

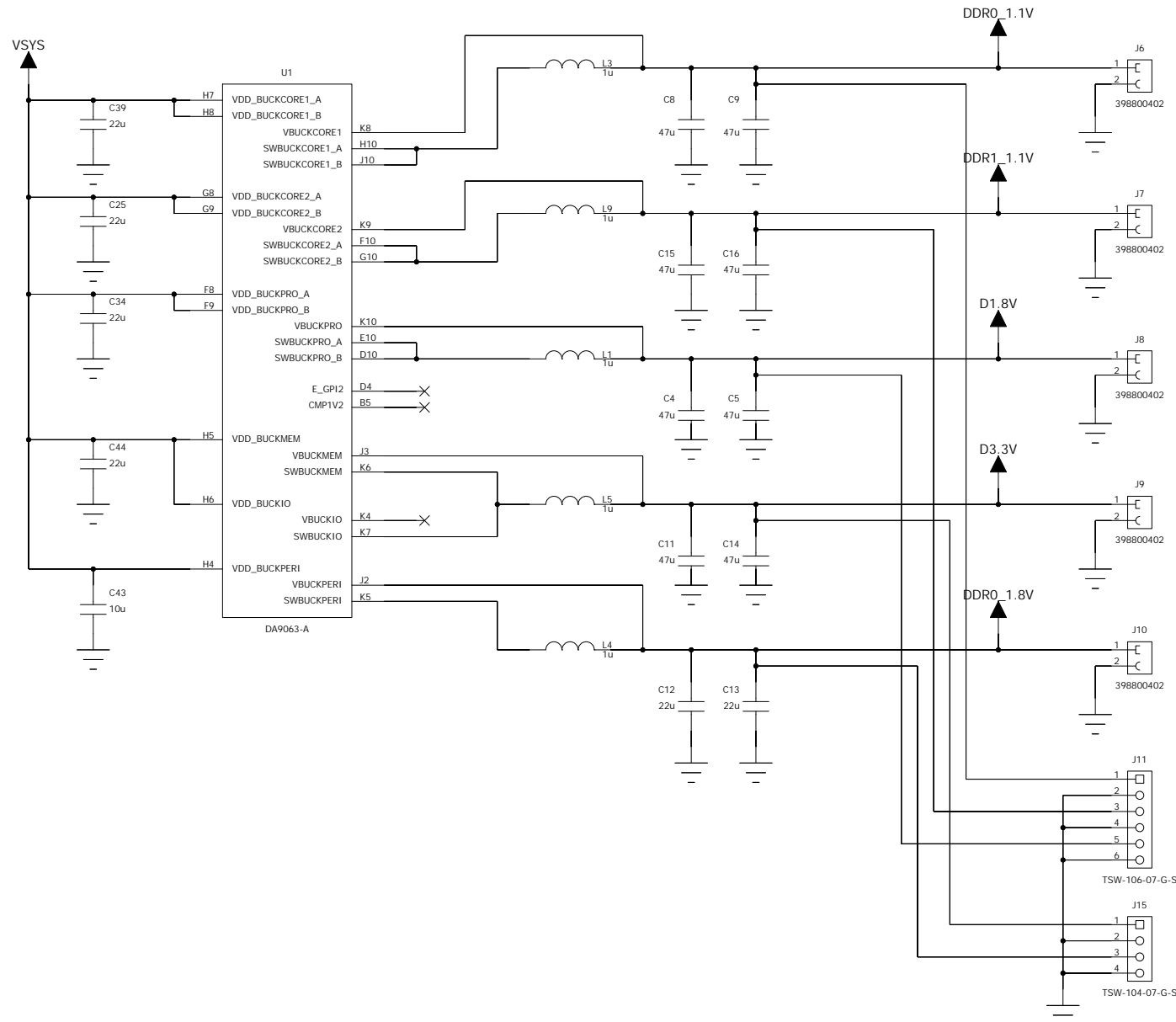
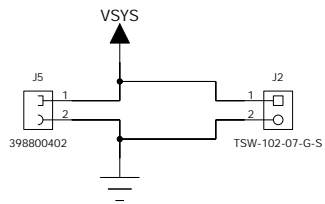
SCHEMATIC TEMPLATE REV.xDxD_V7 (01-06-2017)		CONFIDENTIAL		SIZE		TITLE: Renesas R-Car-M3 ref design			
DESIGNED BY: RP RJ		INFORMATION		A3L		VARIANT:			
REVIEWED BY: RP		SAVED DATE: 13/02/2018:11:06		<div> <div>176-21-A</div> <div>1 000</div> <div>A</div> </div> <div> <div>BOARD No.</div> <div>Sch Rev</div> <div>Variant No.</div> <div>Var Rev</div> </div>				<div> <div>PAGE:</div> <div>of</div> </div> <div> <div>1</div> <div>5</div> </div>	
		REVIEWED DATE: 16-08-2017		<div> <div>SHEET NAME:</div> <div>MECH_REVHIST</div> </div>					



VLDO_SD0 / VLDO_SD1
(1.8V / 3.3V max 310mA)
0.95V - 3.44V (50mV STEP)

VLDO_2.5V
(2.5V max 300mA)
0.9V - 3.5V (50mV STEP)

VDDCORE
(2.5V)
For Dialog internal use only



VDD_DDR0 TRACE WIDTH IS 2mm
(1.1V max 2A)
0.300V - 1.570V (10mV STEP)

VDD_DDR1 TRACE WIDTH IS 2mm
(1.1V max 2A)
0.300V - 1.570V (10mV STEP)

D1.8V TRACE WIDTH IS 3mm
(1.8V max 3A)
0.53V - 1.8V (10mV STEP)

D3.3V TRACE WIDTH IS 3mm
(3.3V max 3A)
0.8V - 3.34V (20mV STEP)

DDR0_1.8V
(1.8V max 0.6A)
0.8V - 3.34V (20mV STEP)

